



EM01: Essential Embedded Systems Design with Xilinx FPGA

EM01: Esencial Sistemas Embebidos en FPGA de Xilinx

Language: The classes are in Spanish, but working material is in English (available also in English at In-Company).

Who Should Attend? Engineers who are interested in developing embedded systems with the Xilinx Zynq All Programmable SoC or MicroBlaze soft processor core using the Embedded Development Flow from Xilinx

Duration: 24 h (3 days, 8 h/day).

Prerequisites: FPGA design experience; Knowledge from “Essentials of FPGA Design course (FP01)” or equivalent. Basic understanding of C programming; Fundamental microprocessors understanding; Some HDL modeling experience.

Introduction: This course update you on developing embedded systems using the Vivado® Design Suite. Features and capabilities of both the Zynq® All Programmable System on a Chip (AP-SoC) and the MicroBlaze™ soft processor are covered in lectures and labs. Additionally general embedded concepts, tools, and techniques are studied. The hands-on labs provide you with experience designing, expanding, and modifying an embedded system, including adding and simulating a custom AXI-based peripheral using bus functional model (BFM) simulation.

Skills Gained: After completing this training, you will know how to:

- Understand the key concepts to build a embedded system in a FPGA.
- Describe the various tools that encompass a Xilinx embedded design

- Rapidly architect an embedded system containing a MicroBlaze™ or Cortex™-A9 processor by using the Vivado IP Integrator and Customization Wizard
- Develop software applications using the Eclipse-based Software Development Kit (SDK)
- Create and integrate an IP-based processing system component in the Vivado Design Suite
- Design and add a custom AXI interface-based peripheral to the embedded processing system
- Simulate a custom AXI interface-based peripheral using a bus functional model (BFM)
- Understand different ways to optimize system performance in FPGA based embedded developments.

Material: Each student will have a computer with the development tools (Vivado 2015.x), documentation, repository whit exercises (and solutions) and a FPGA development board for exercises that require it.

Related Courses:

FP01: Essential Vivado Design Suite: 7-Series, TCL, Static Timing Analysis, Constraints.

EM01: Embedded Systems Design with Xilinx FPGA (this course).

EM02: Advanced Features and Techniques of Embedded Systems Design

Other Xilinx Technologies courses:

HL01: HDL Logical Synthesis and Simulation for Xilinx FPGA design



FP02: Advanced Vivado Design Suite: Advanced Timing, Tools and Techniques.

EML1: Build a Linux distribution for Xilinx FPGA

HLS1: High Level Synthesis for Xilinx FPGAs using Vivado-HLS

DSP1: DSP Design Using System Generator

SDS1: SDSoc development environment

SDA1: SDAccel for algorithm acceleration

Dates, location and registration:

Please visit www.electraining.org

Price:

EM01: 1650 € Includes cafes and lunches

Course Packs and Discounts:

EM01 + EM02: 2640 € (-20%)

FP01 + FP02: 2640 € (-20%)

HL01 + FP01: 2240 € (-20%)

HL01 + FP01 + FP02: 3340 € (-25%)

HL01+FP01+FP02+EM01+EM02: 4960 € (-36%)

EML1 coming from FP/ EM: 1320 € (-20%)

HLS1 coming from FP/ EM: 1320 € (-20%)

Additional discounts:

Previous ElectraTraining course 5%

Prior Xilinx technology course in last year: 10%

More than one participant from the same company.

It is possible to use Xilinx Training Credits.